

Abstract of the Disclosur

A differential output circuit for improving the bandwidth thereof is provided. The differential output circuit is composed of first and second inputs, first and second outputs, a resistor element connected between the first and second outputs, first and second N-channel MISFETs, and first and second P-channel MISFETs. The first and second inputs respectively receives first and second complementary input signals. The first N-channel MISFET has a source connected to the first input, a gate receiving a power supply potential, and a drain connected to the first output. The second N-channel MISFET has a source connected to the second input, a gate receiving the power supply potential, and a drain connected to the second output. The first P-channel MISFET has a source receiving the power supply potential, a gate connected to the second input, and a drain connected to the first output. The second P-channel MISFET has a source receiving the power supply potential, a gate connected to the first input, and a drain connected to the second output.